

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2306108	(test\$3 verify\$3 verification)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L2	2219	(FPGA ASIC PLD EPLD CPLD "field programmable gate array" field-programmable-gate array "application specific integrated circuit" application-specific-integrated circuit "application specific IC" ((erasable complex) ADJ (PLD "programmable logic device")))) WITH ((module cell block) NEAR5 (property attribute))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L3	959	L2 AND L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L4	5318	JTAG	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L5	28	L3 AND L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L6	5059587	(FPGA ASIC PLD EPLD CPLD "field programmable gate array" field-programmable-gate array "application specific integrated circuit" application-specific-integrated circuit "application specific IC" ((erasable complex) ADJ (PLD "programmable logic device"))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L7	74232	(module cell block) NEAR5 (property attribute)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57

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L8	17274	L6 AND L7 AND L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L9	31082	("711").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/30 21:57
L10	239	L8 AND L9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L11	2306108	(test\$3 verify\$3 verification)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L12	2219	(FPGA ASIC PLD EPLD CPLD "field programmable gate array" field-programmable-gate array "application specific integrated circuit" application-specific-integrated circuit "application specific IC" ((erasable complex) ADJ (PLD "programmable logic device")))) WITH ((module cell block) NEAR5 (property attribute))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L13	959	L12 AND L11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57
L14	5318	JTAG	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:57

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L17	110528	(FPGA ASIC PLD EPLD CPLD "field programmable gate array" "field-programmable-gate array" "application specific integrated circuit" "application-specific-integrated circuit" "application specific IC" (("erasable complex") ADJ (PLD "programmable logic device"))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:58
L18	35608	L17 and (test or verification)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:58
L19	31120	L18 and ((module or sub-module or submodule or block or sub-block))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:58
L20	77	L19 and (data-structure or datastructure)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/30 21:58